

COMPENSATOR CIRCUIT FOR AN OPTICAL STORAGE DEVICE

Abstract

A compensator circuit comprises a phase-lead compensator for receiving an error signal generated by an optical storage device and generating a phase-lead error signal, a band-pass filter connected in parallel with the lead compensator for magnifying a rotating frequency error signal and generating a filtered signal, and an adder for synthesizing the phase-lead error signal and the filtered signal so as to reduce a steady-state error of the error signal. The compensator circuit does not comprise any phase-lag compensator.